

# PATENT ABSTRACTS OF JAPAN

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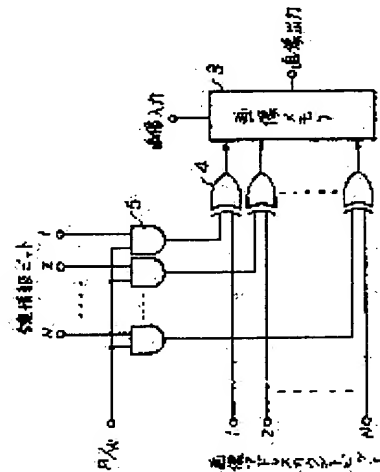
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## (54) SIGNAL SCRAMBLE/DESCRAMBLE CIRCUIT

### (57)Abstract:

**PURPOSE:** To increase the diversity of the transposition of pictures and to secure privacy by ciphering the address showing the position of each single part of a series of signals after converting it in 1:1 into another address by means of a ciphering process.

**CONSTITUTION:** At the transmitter side the signals are sent in the form of scramble picture signals together with the key information needed for ciphering. At the receiver side the received scramble picture signals are written to a field picture by means of the transposition address produced by a ciphering circuit by the key information in the same way as the transmitter side. Thus the transposition pictures are arranged again as the sequential pictures. Then the restored picture signals are obtained. For the picture address count bits (N bits), an N-bit block is ciphered by an exclusive OR circuit XOR4 with the key information bit. Then the transposition pictures are obtained by reading the picture signals out of a field picture memory 3 using a transposition address converted by ciphering.



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